

figure 1

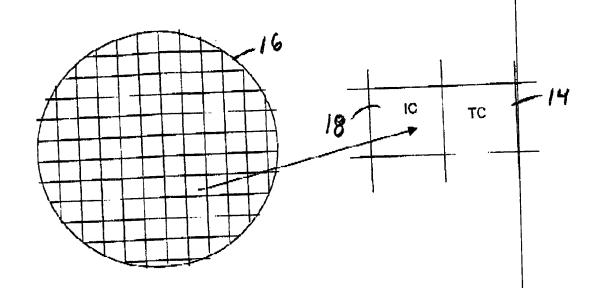
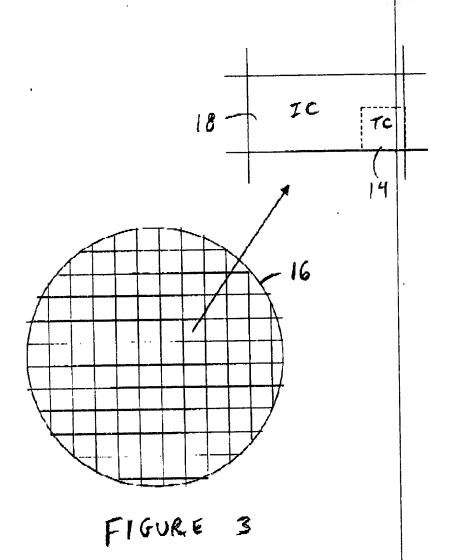


FIGURE 2



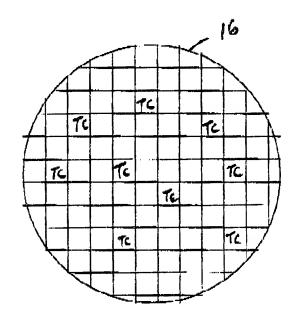


FIGURE 4

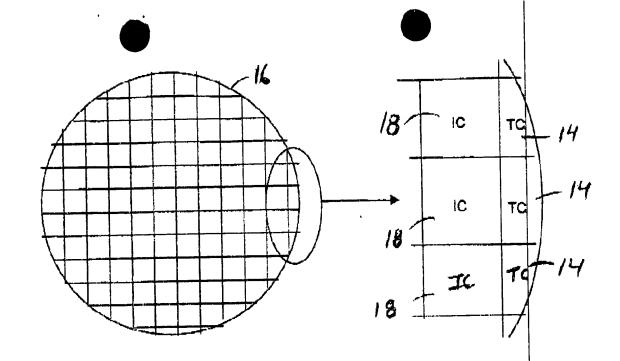


FIGURE 5

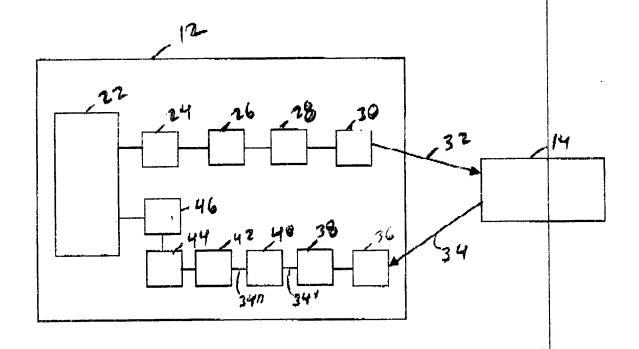


FIGURE 6

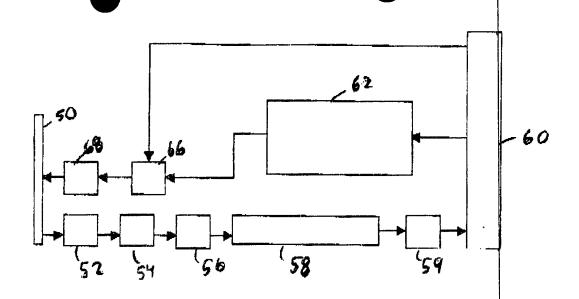


FIGURE 7.

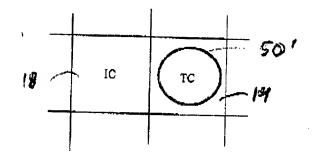
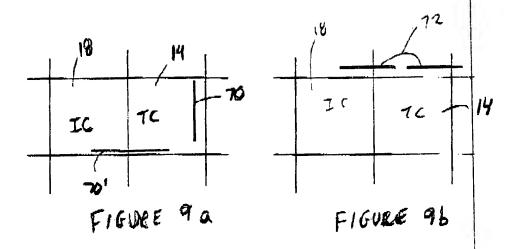
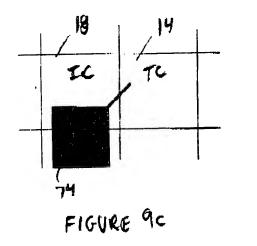
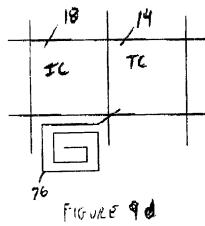


FIGURE 8







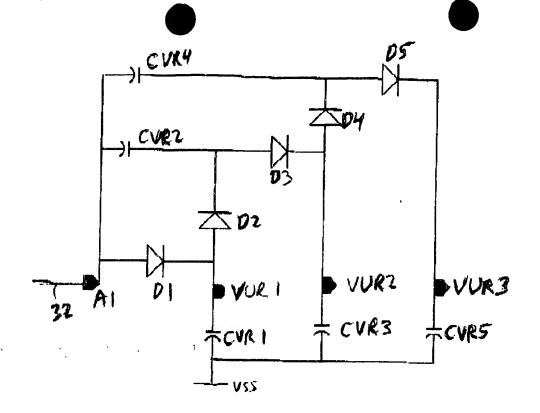


FIGURE 10.

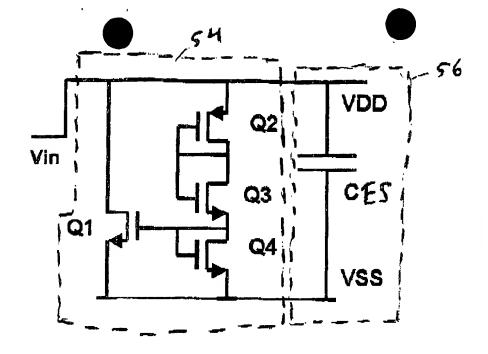


FIGURE 11

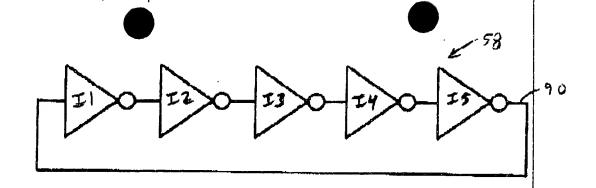


FIGURE 12

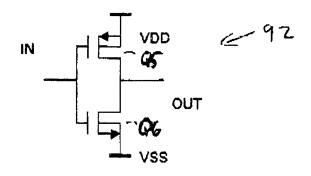


FIGURE 13

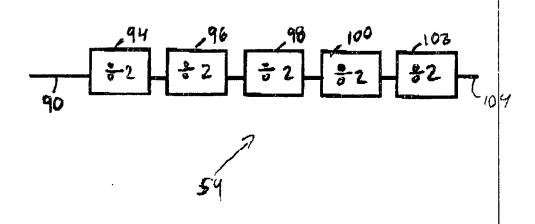


FIGURE 14

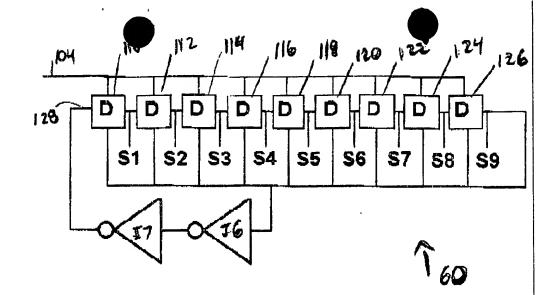


FIGURE 15

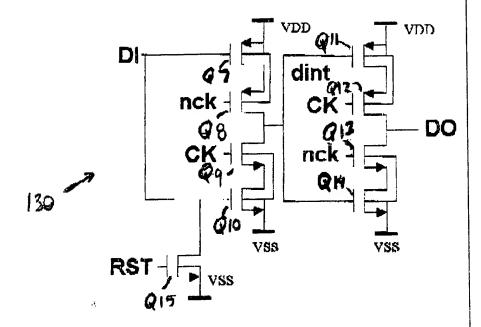
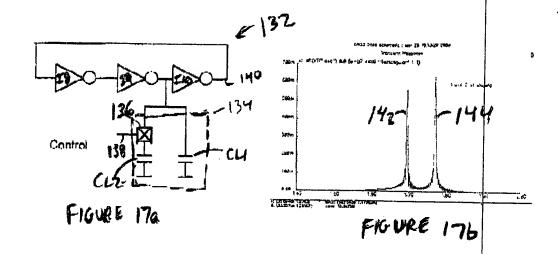
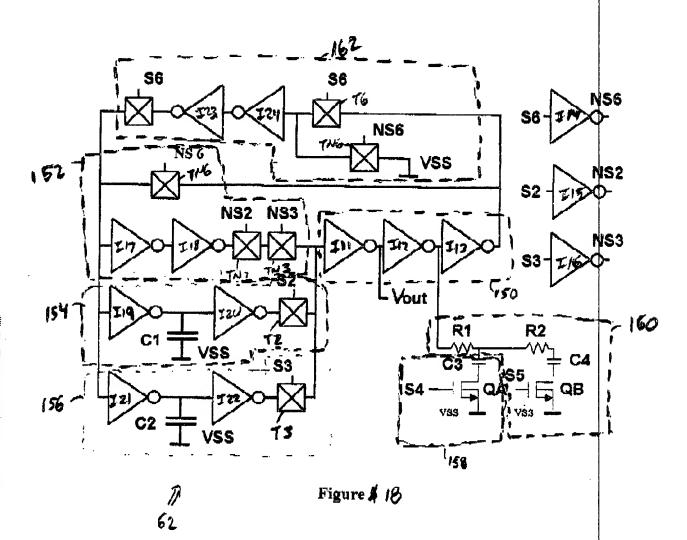


FIGURE 16





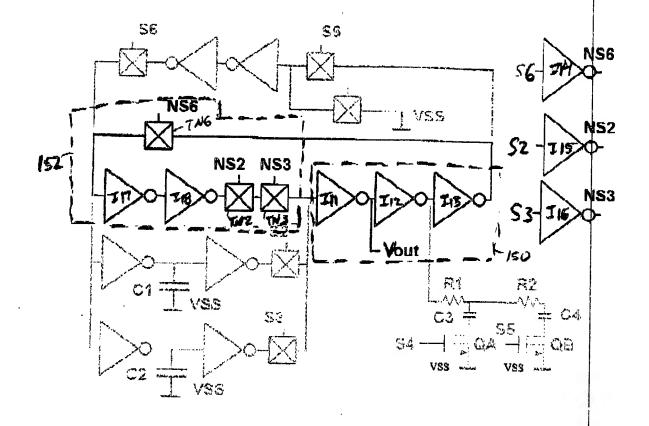


FIGURE 20

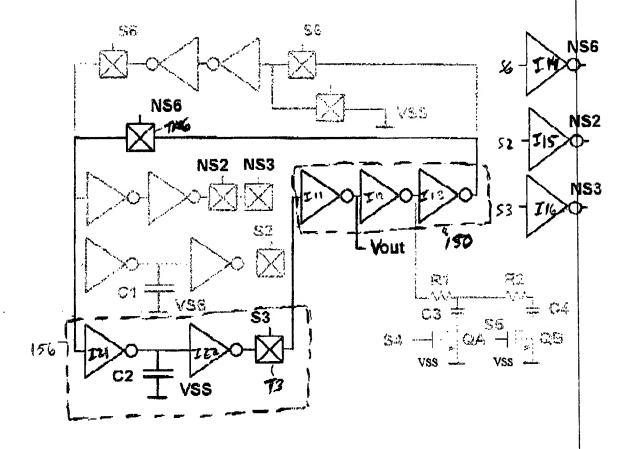


FIGURE 22

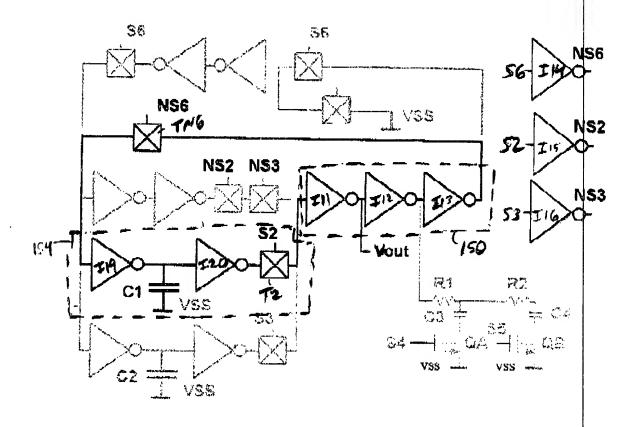


FIGURE 21

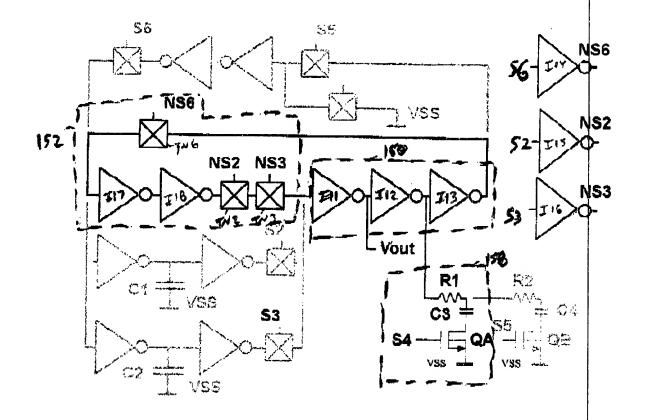


FIGURE 23

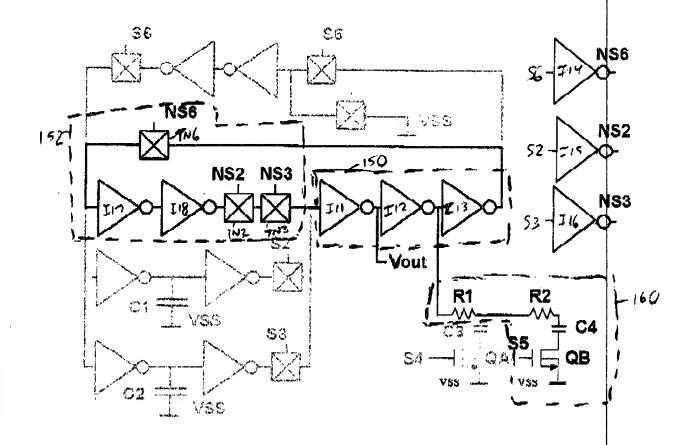


FIGURE 24

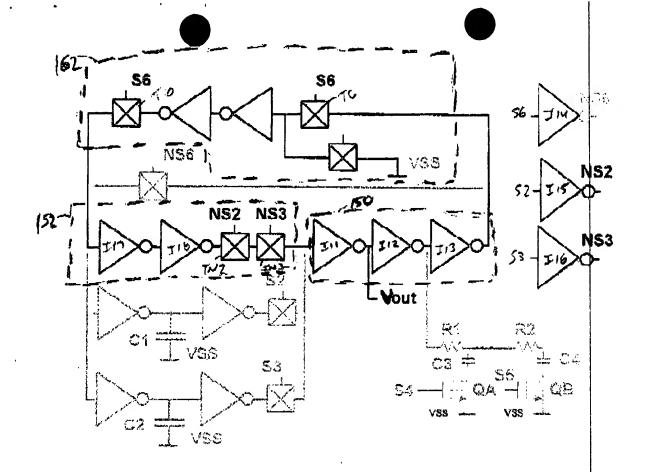


FIGURE 25

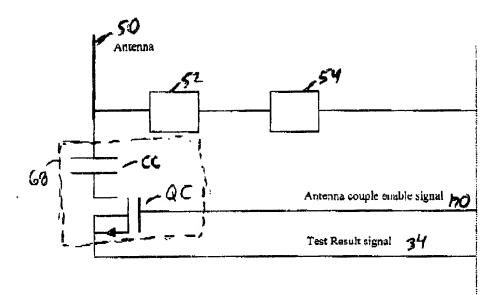


FIGURE 26

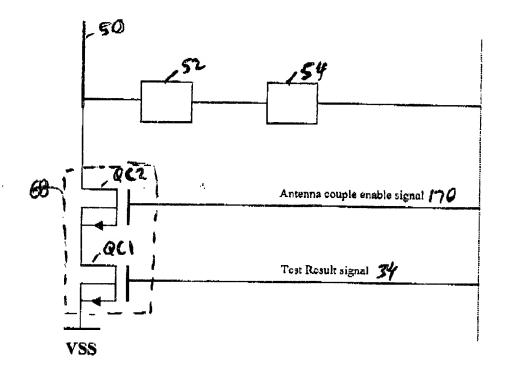


FIGURE 27

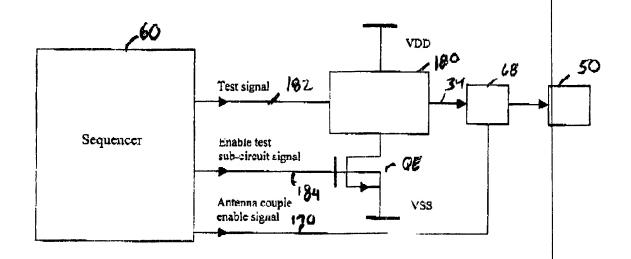


FIGURE 28

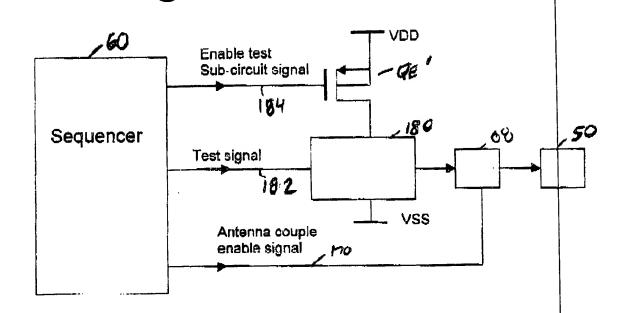


FIGURE 29

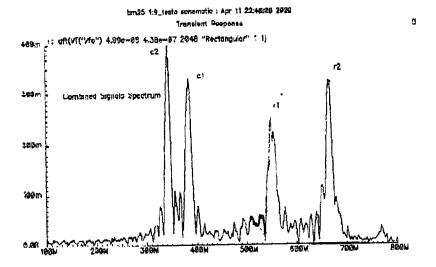
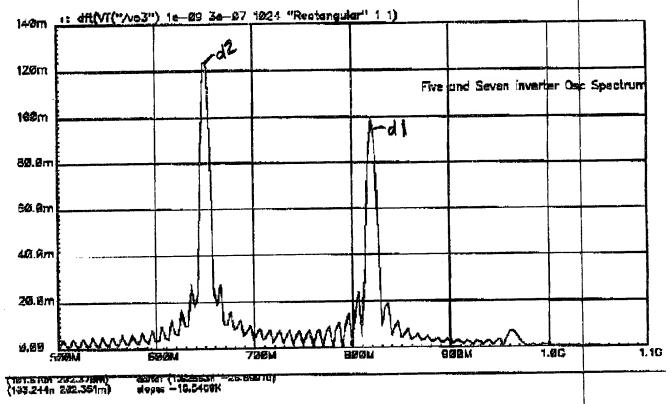


FIGURE 30a

sc982 Sinvess schematte : Mar 26 19:55:38 2698 Transient Response



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Figure 306

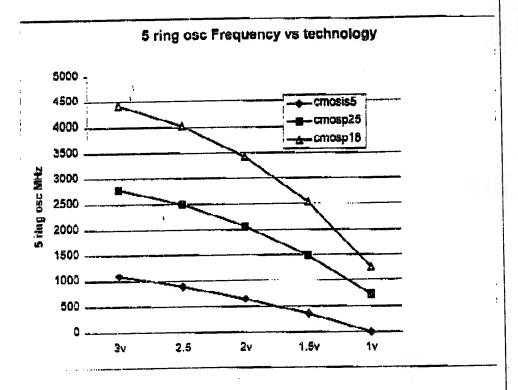


FIGURE 31